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DATE: January 23, 2004

TO:

Examiner Steven Rao

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FROM:

Jeff Kirshner

RE:

728-208

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<u>6</u>

MESSAGE:

Per your request please see the attached Supplemental Amendment.

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PATENT APPLICATION Attorney Docket: 728-208 YOR9-2001-0270 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Louis L. HSU, et al.

Examiner: Rao, Shrinivas

Serial No.:

09/880,223

Group: Art Unit 2814

Filed:

June 13, 2001

Dated: January 23, 2004

For:

T-RAM CELL HAVING A BURIED VERTICAL THYRISTOR AND A PSEUDO-TFT TRANSFER GATE AND A METHOD FOR FABRICATING THE SAME

AMENDMENT TRANSMITTAL FORM

Sir:

Transmitted herewith is an amendment in the above-identified application.

- [] Small entity status of this application under 37 C.F.R. §§1.9 and 1.27 has been established by a verified statement previously submitted.
- [] A verified statement to establish small entity under 37 C.F.R. §§1.9 and 1.27 is enclosed.
- [X] No additional fee is required.

For	Claims Remaining After Amendment	Highest No. Previously Paid For	Present Extra	Rate (Small Entity)	Addit. Fee	Rate	Addit. Fee
TOTAL CLAIMS	5	28	0	x 9 =	\$0	x 18 =	\$0
INDEPENDENT CLAIMS	1	0	0	x 43 =	\$0	x 86 =	\$0
[] First Presentation of Multiple Dep. Claim				140		290	\$0

* If the entry in Col. 1 is less than entry in Col. 2, write "0" in Col. 3.

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Respectfully submitted,

Paul JUFarrell

Reg No. 33,494

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